



DC6288FT ISP programming interface

AppNote302

Document Revision 1.3

January, 2020

Table of Content

1	INTRODUCTION	3
2	HARDWARE.....	4
3	DC6688SLP-USB REV3.1.....	7
4	DC6688SLP-USB REV3.2.....	9
	4.1 SINGLE DEVICE MODE	9
	4.2 MULTIPLE DEVICE MODE.....	9
5	DCT-SLP REV3.3	11
	5.1 SINGLE DEVICE MODE	11
	5.2 MULTIPLE DEVICE MODE.....	11
6	DCT-EDP REV3.X	13
	REVISION HISTORY	14

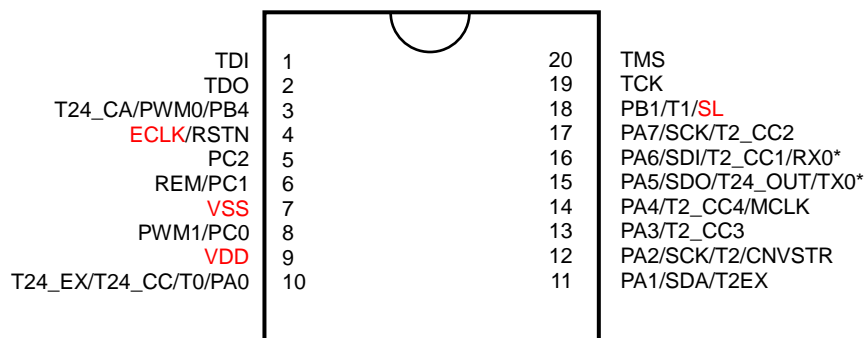
1 Introduction

This document describes how DC6288FT to do programming.

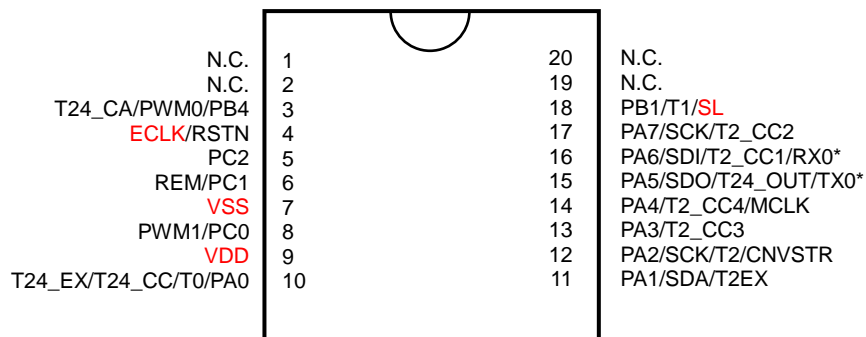
2 Hardware

To do trimming during programming stage, 4 pads are required on PCB to complete this process, and the pins are highlighted in red.

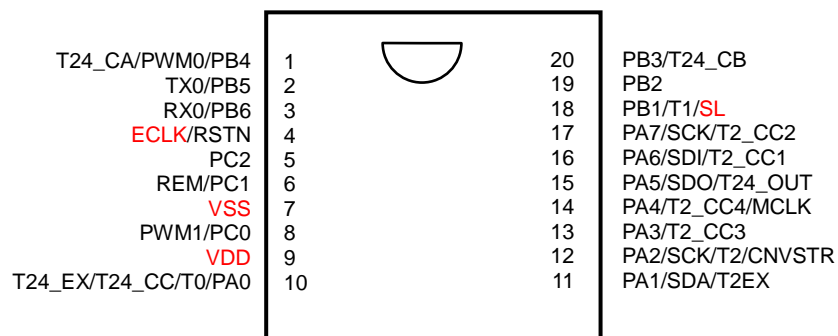
TSSOP20-E



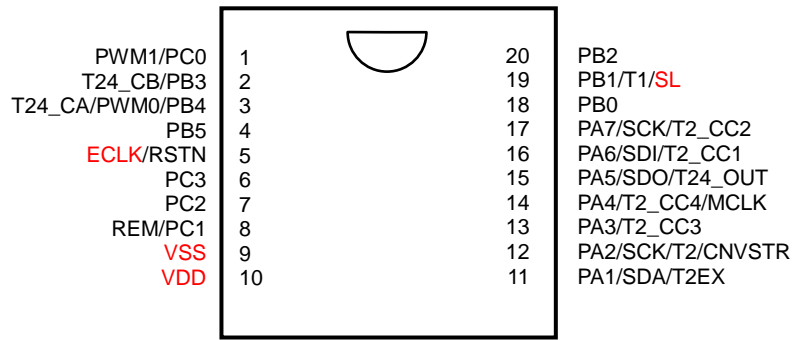
TSSOP20-L



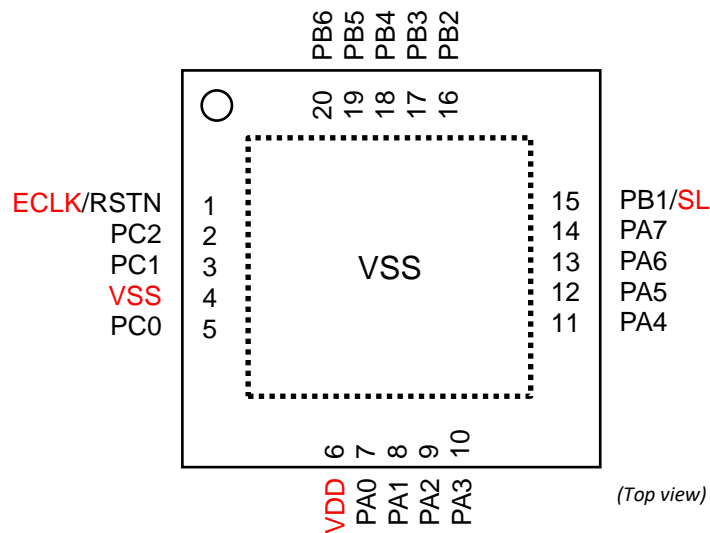
TSSOP20-M



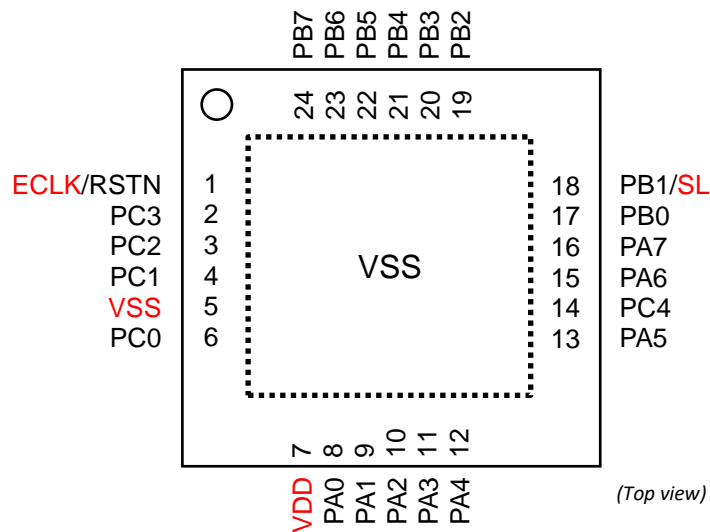
TSSOP20-R



(QFN20)



(QFN24)

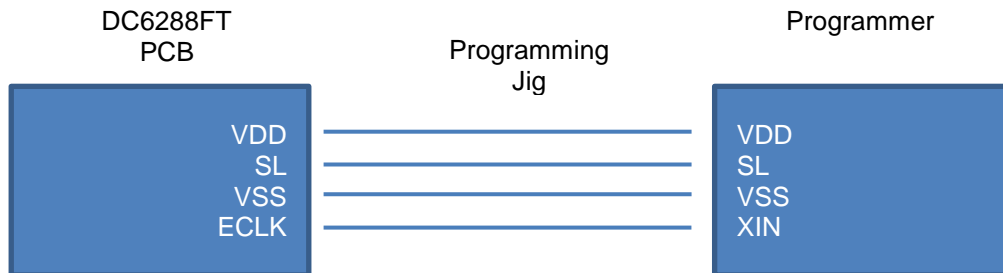


The programmer requires any one of below:

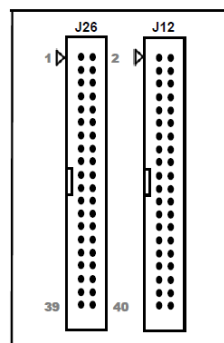
1. DC6688SLP-USB Rev3.1/3.2.
2. DCT-SLP Rev3.3
3. DCT-EDP Rev3.x

Item 1 and 2 above can be able to program up to 8 devices (Device 1 ~ 8).
The pin assignment on the programmer is shown in the following section.

3 DC6688SLP-USB Rev3.1



SLP Rev3.1



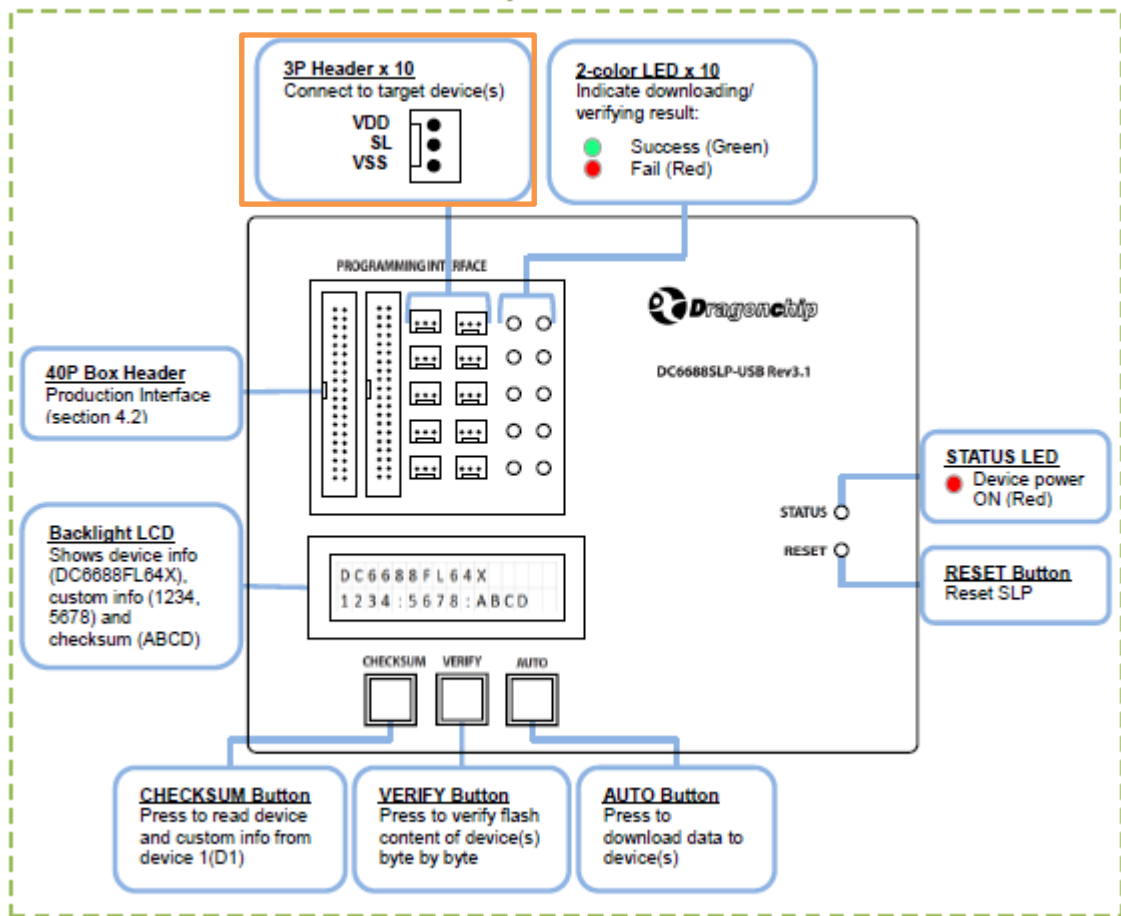
J26 Pin Assignment

Pin	Name	Connection	Pin	Name	Connection
1	NC	-	2	GND	GND
3	NC	-	4	NC	-
5	NC	-	6	NC	-
7	NC	-	8	NC	-
9	NC	-	10	NC	-
11	NC	-	12	NC	-
13	NC	-	14	NC	-
15	NC	-	16	NC	-
17	NC	-	18	GND	GND
19	GND	GND	20	NC	-
21	XIN1	D1 XIN / XIN2 pin	22	GND	GND
23	XIN2	D2 XIN / XIN2 pin	24	GND	GND
25	XIN3	D3 XIN / XIN2 pin	26	GND	GND
27	XIN4	D4 XIN / XIN2 pin	28	GND	GND
29	XIN5	D5 XIN / XIN2 pin	30	GND	GND
31	XIN6	D6 XIN / XIN2 pin	32	GND	GND
33	XIN7	D7 XIN / XIN2 pin	34	GND	GND
35	XIN8	D8 XIN / XIN2 pin	36	GND	GND
37	XIN9	D9 XIN / XIN2 pin	38	GND	GND
39	XIN10	D10 XIN / XIN2 pin	40	GND	GND

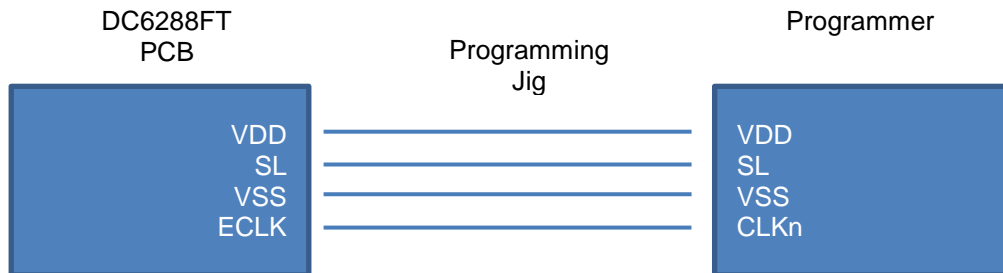
Each XIN pin for 1 IC's ECLK

VDD / SL / VSS pins connection is shown below.

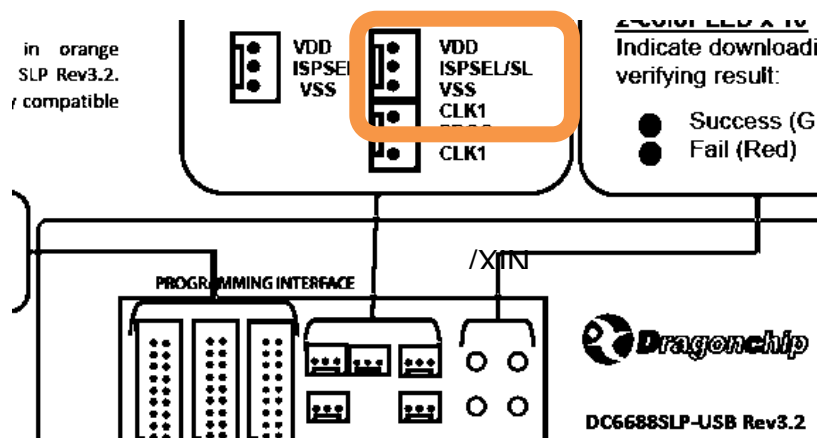
Top View



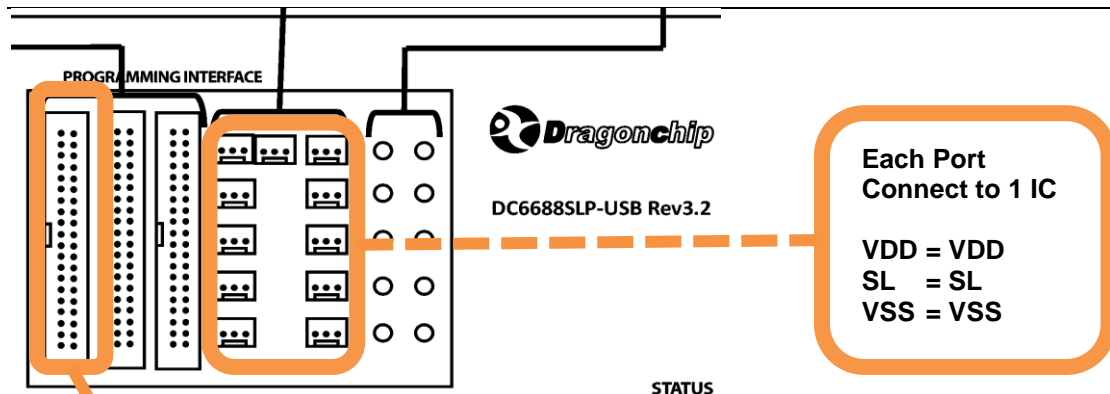
4 DC6688SLP-USB Rev3.2



4.1 Single Device Mode



4.2 Multiple Device Mode



STATUS

J27 Pin Assignment

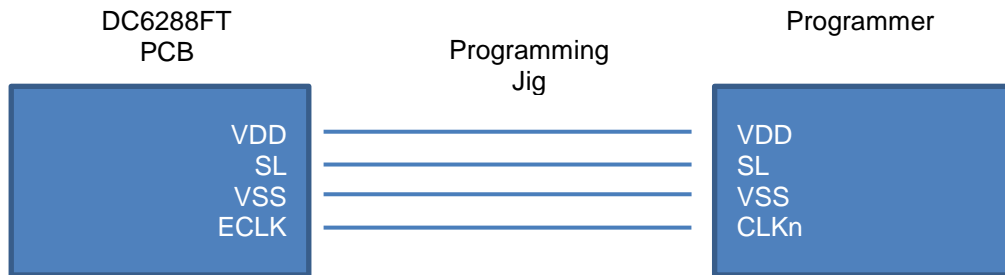
Pin	Name	Connection
1	NC	NC
2	NC	NC
3	NC	NC
4	NC	NC
5	NC	NC
6	NC	NC
7	NC	NC
8	NC	NC
9	NC	NC
10	NC	NC
11	NC	NC
12	NC	NC
13	NC	NC
14	NC	NC
15	PROG_A	PROG of D1-D5
16	PROG_B	PROG of D6-D10
17	GND	GND
18	GND	
19	GND	
20	NC	
21	GND	
22	GND	
23	GND	
24	GND	
25	GND	
26	GND	
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37	GND	
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40	GND	

J26 Pin Assignment

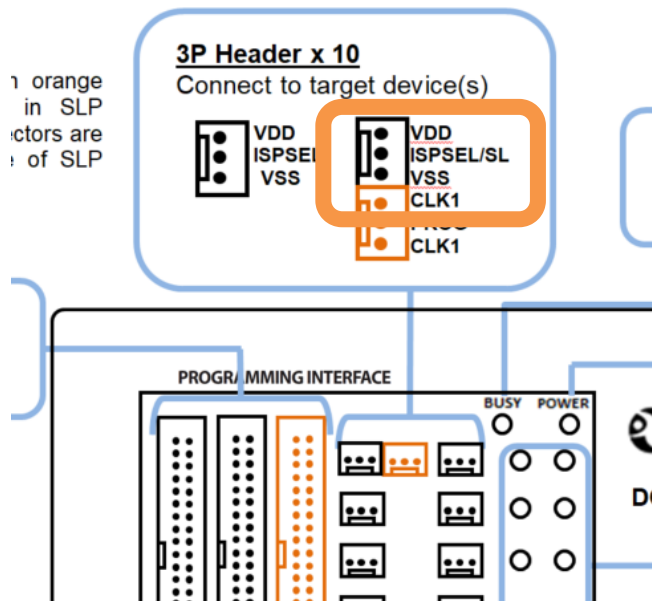
Pin	Name	Connection
1	LCD_P9	LCD module pin 9 (DB2)
3	LCD_P8	LCD module pin 8 (DB1)
5	LCD_P7	LCD module pin 7 (DB0)
7	LCD_P6	LCD module pin 6 (E)
9	LCD_P5	LCD module pin 5 (R/W)
11	LCD_P4	LCD module pin 4 (RS)
13	LCD_P3	LCD module pin 3 (VEE)
15	LCD_P2	LCD module pin 2 (VCC)
17	LCD_P1	LCD module pin 1 (VSS)
19	GND	GND
21	CLK1	D1 XIN/ ECLK pin
23	CLK2	D2 XIN/ ECLK pin
25	CLK3	D3 XIN/ ECLK pin
27	CLK4	D4 XIN/ ECLK pin
29	CLK5	D5 XIN/ ECLK pin
31	CLK6	D6 XIN/ ECLK pin
33	CLK7	D7 XIN/ ECLK pin
35	CLK8	D8 XIN/ ECLK pin
37	CLK9	D9 XIN/ ECLK pin
39	CLK10	D10 XIN/ ECLK pin

Each XIN pin for 1 IC's ECLK

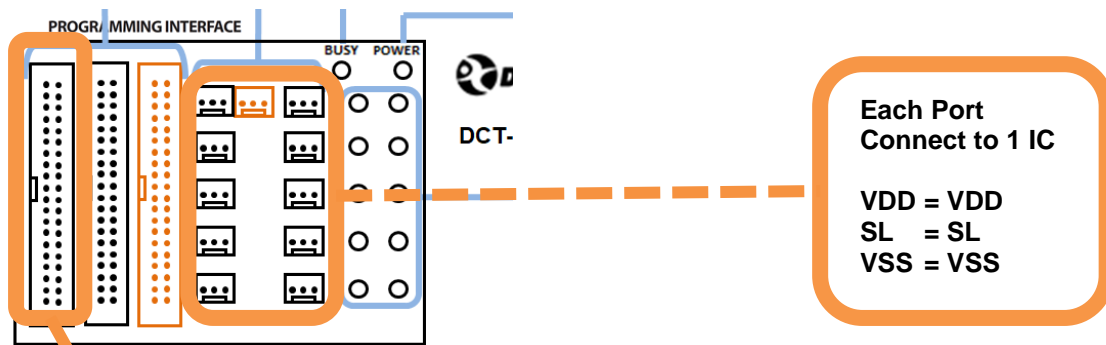
5 DCT-SLP Rev3.3



5.1 Single Device Mode



5.2 Multiple Device Mode



J26 Pin Assignment

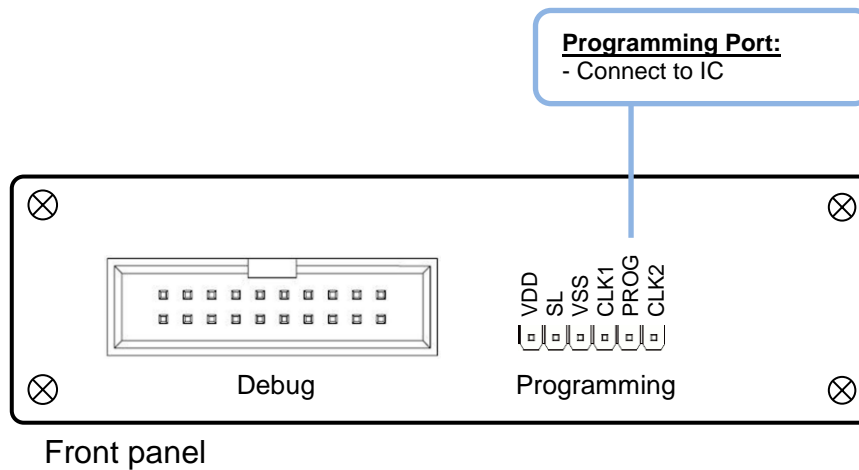
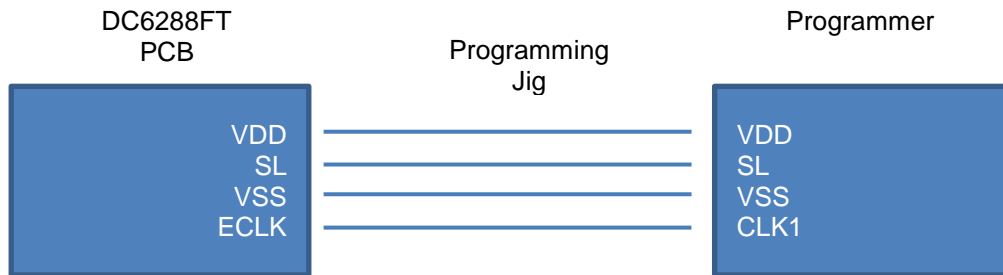
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15	LCD_P2	LCD module pin 2 (VCC)
17	LCD_P1	LCD module pin 1 (VSS)
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25	CLK3	D3 XIN/ ECLK pin
27	CLK4	D4 XIN/ ECLK pin
29	CLK5	D5 XIN/ ECLK pin
31	CLK6	D6 XIN/ ECLK pin
33	CLK7	D7 XIN/ ECLK pin
35	CLK8	D8 XIN/ ECLK pin
37	CLK9	D9 XIN/ ECLK pin
39	CLK10	D10 XIN/ ECLK pin

Each XIN pin for 1 IC's ECLK

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Pin	Name	Connection
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3	NC	NC
5	NC	NC
7	NC	NC
9	NC	NC
11	NC	NC
13	NC	NC
15	PROG_A	PROG of D1-D5
17	PROG_B	PROG of D6-D10
19	GND	GND
20	NC	
22	GND	
24	GND	
26	GND	
28	GND	
30	GND	
32	GND	
34	GND	
36	GND	
38	GND	
40	GND	

6 DCT-EDP Rev3.x



Revision History

Document Rev. No.	Issued Date	Section	Page	Description	Edited By	Reviewed By
1.0	Apr, 2018			First release	Danny Ho	Patrick Li
1.1	Jul, 2018	2		Added QFN24	Danny Ho	Patrick Li
1.2	Jan, 2020	5		Added DCT-SLP Rev3.3	Danny Ho	Patrick Li
		6		Added DCT-EDP Rev3.x		
1.3	Jan, 2020	2		Added TSSOP20-L pin assignment	Danny Ho	Patrick Li

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